### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hsien-Te Kevin Shih

FILED: Concurrently Herewith

FOR: MEHTOD AND CIRCUIT FOR OPTIMIZING

POWER EFFICIENCY IN A DC-DC

CONVERTER

9/11/2003 Date:

# DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Honorable Assistant Commissioner for Patents P.O. BOX 1450

Alexandria, VA 22313-1450

## SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

> Respectfully submitted, Hsien-Te Kevin Shih

James J. Stipanuk

Reg. #44,358

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Intellectual Property Dept. P.O. Box 62890 - A700 Phoenix, AZ 85062-2890

Customer #: 27255

ON Semiconductor

# F rm PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE

STATEMENT (Use Several Sheets if Necessary)

ATTY. DOCKET NO.	SERIAL NO.	
Ons00496		
APPLICANT		
Hsien-Te Kevin Shih		
FILING DATE	GROUP	
CONCURRENTLY		

REFERENCE DESIGNATION	U.S. PATENT DOCUMENTS

Examiner Initial	Do	ocume	ent	Nur	nbe	r				Date	Name	Class	Sub- Class	Filing date (If approp rirate)
I AA	_	T	6	5	1	5	4	6	3	2/4/2003	Ling	G05F	3/16	
AB	_		6	5	3	5	4	0	0	3/18/2003	Bridge	H02M	3/335	
AC	_		6	3	9	6	2	5	0	5/28/2002	Bridge	G05F	1/40	
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### FOREIGN PATENT DOCUMENTS

Examiner	xaminer Document #			Date	Country	Cla	ass/subclass	Trans	Translation			
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	OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)	
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EXAMINER DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.